



15-441 Computer Networking

Lecture 8 – IP Packets, Routers

Outline

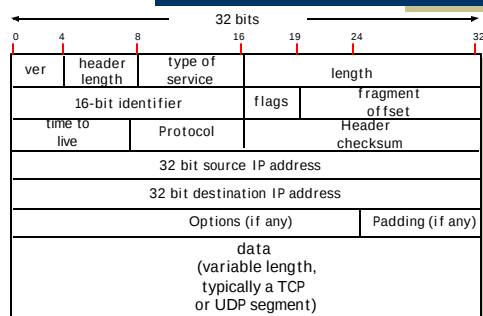


- IP Packet Format
- NAT
- IPv6
- Router Internals
- Route Lookup

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2

IPv4 Header – RFC791 (1981)



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3

IP Header Fields



- Version → 4 for IPv4
- Header length (in 32 bit words)
 - Minimum value is 5 (header without any options)
- Length of entire IP packet in octets (including header)
- Identifier, flags, fragment offset → used primarily for fragmentation
- Time to live
 - Must be decremented at each router
 - Packets with TTL=0 are thrown away
 - Ensure packets exit the network

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4

IP Header Fields



- Protocol
 - Demultiplexing to higher layer protocols
 - TCP = 6, ICMP = 1, UDP = 17...
- Header checksum
 - Ensures some degree of header integrity
 - Relatively weak – 16 bit
- Source/Dest address
- Options
 - E.g. Source routing, record route, etc.
 - Performance issues
 - Poorly supported

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5

IP Type of Service



- Typically ignored
- Values
 - 3 bits of precedence
 - 1 bit of delay requirements
 - 1 bit of throughput requirements
 - 1 bit of reliability requirements
- Replaced by DiffServ

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6

ICMP: Internet Control Message Protocol

- Used by hosts, routers, gateways to communicate network-level information
- Error reporting: unreachable host, network, port, protocol
- Echo request/reply (used by ping)
- Network-layer "above" IP:
 - ICMP msgs carried in IP datagrams
- ICMP message: type, code plus first 8 bytes of IP datagram causing error

Type	Code	description
0	0	echo reply (ping)
3	0	dest. network unreachable
3	1	dest host unreachable
3	2	dest protocol unreachable
3	3	dest port unreachable
3	6	dest network unknown
3	7	dest host unknown
4	0	source quench (congestion control - not used)
8	0	echo request (ping)
9	0	route advertisement
10	0	router discovery
11	0	TTL expired
12	0	bad IP header

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7

Fragmentation

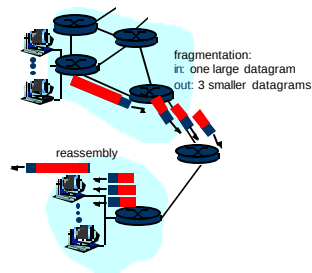
- IP packets can be up to 64KB
- Different link-layers have different MTUs
- Split IP packet into multiple fragments
 - IP header on each fragment
 - Intermediate router may fragment as needed

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8

IP Fragmentation & Reassembly

- Network links have MTU (max. transfer size) - largest possible link-level frame.
 - different link types, different MTUs
- Large IP datagram divided ("fragmented") within net
 - one datagram becomes several datagrams
 - IP header bits used to identify, order related fragments



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9

Reassembly

- Where to do reassembly?
- End nodes
 - Avoids unnecessary work where large packets are fragmented multiple times
- Dangerous to do at intermediate nodes
 - How much buffer space required at routers?
 - What if routes in network change?
 - Multiple paths through network
 - All fragments only required to go through destination

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10

Fragmentation Related Fields

- Length
 - Length of IP fragment
- Identification
 - To match up with other fragments
- Flags
 - Don't fragment flag
 - More fragments flag
- Fragment offset
 - Where this fragment lies in entire IP datagram
 - Measured in 8 octet units (13 bit field)

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11

IP Fragmentation and Reassembly

length	ID	fragflag	offset
=4000	=x	=0	=0

One large datagram becomes several smaller datagrams

length	ID	fragflag	offset
=1500	=x	=1	=0
length	ID	fragflag	offset
=1500	=x	=1	=1480
length	ID	fragflag	offset
=1040	=x	=0	=2960

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12

Fragmentation is Harmful



- Uses resources poorly
 - Forwarding costs per packet
 - Best if we can send large chunks of data
 - Worst case: packet just bigger than MTU
- Poor end-to-end performance
 - Loss of a fragment
- Reassembly is hard
 - Buffering constraints

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13

Path MTU Discovery



- Hosts dynamically discover minimum MTU of path
- Algorithm:
 - Initialize MTU to MTU for first hop
 - Send datagrams with Don't Fragment bit set
 - If ICMP "pkt too big" msg, decrease MTU
- What happens if path changes?
 - Periodically (>5mins, or >1min after previous increase), increase MTU
- Some routers will return proper MTU
- MTU values cached in routing table

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14

Outline



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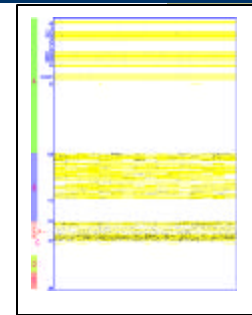
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15

IP Address Utilization ('98)



- Address space depletion
 - In danger of running out of classes A and B
- 32-bit address space completely allocated by 2008
- Two solutions
 - NAT
 - IPv6



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16

Network Address Translation (NAT)

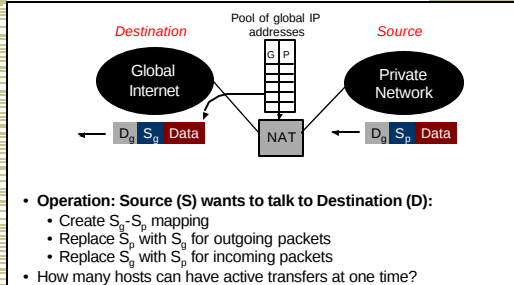


- Possible solution to address space exhaustion
 - Kludge (but useful)
- Sits between your network and the Internet
- Translates local network layer addresses to global IP addresses
- Has a pool of global IP addresses (less than number of hosts on your network)
- Uses special unallocated addresses (RFC 1597) locally
 - 10.0.0.0/8, 172.16.0.0/12, 192.168.0.0/16

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17

NAT Illustration



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18

Problems with NAT



- What if we only have few (or just one) IP address?
 - Use Network Address & Port Translator (NAPT)
- NAPT translates:
 - Translates $\text{addr}_{\text{private}} + \text{flow info}$ to $\text{addr}_{\text{global}} + \text{new flow info}$
 - Uses TCP/UDP port numbers
 - Potentially thousands of simultaneous connections with one global IP address

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19

Problems with NAT



- Hides the internal network structure
 - Some consider this an advantage
- Some protocols carry addresses
 - E.g., FTP carries addresses in text
 - What is the problem?
- Must update transport protocol headers (port number & checksum)
- Encryption
- No inbound connections

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20

Outline



- IP Packet Format
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- **IPv6**
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21

IPv6



- Primary objective bigger addresses
 - Addresses are 128bit → What about header size!!!
- Simplification
 - Header format helps speed processing/forwarding
 - Header changes to facilitate QoS
 - Removes infrequently used parts of header
 - 40byte fixed size vs. 20+ byte variable

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22

IPv6 Changes

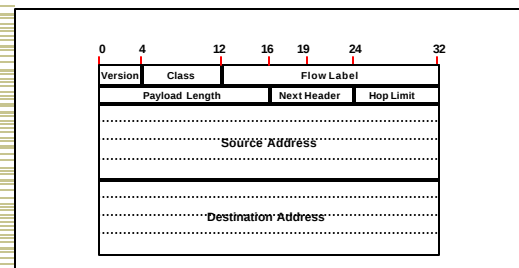


- IPv6 removes checksum
 - Relies on upper layer protocols to provide integrity
- IPv6 eliminates fragmentation
 - Requires path MTU discovery
 - Requires 1280 byte MTU

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23

IPv6 Header



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24

IPv6 Changes

- TOS replaced with traffic class octet
- Flow label
 - Identify datagrams in same "flow." (concept of "flow" not well defined)
 - Help soft state systems
 - Maps well onto TCP connection or stream of UDP packets on host-port pair
- Easy configuration
 - Provides auto-configuration using hardware MAC address to provide unique base

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25

IPv6 Changes

- Protocol field replaced by next header field
 - Support for protocol demultiplexing as well as option processing
- Option processing
 - Options are added using next header field
 - Options header does not need to be processed by every router
 - Large performance improvement
 - Makes options practical/useful
- Additional requirements
 - Support for security
 - Support for mobility

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26

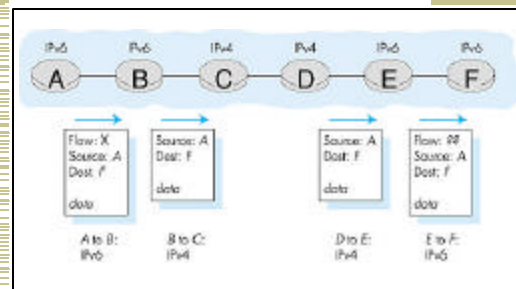
Transition From IPv4 To IPv6

- Not all routers can be upgraded simultaneously
 - No "flag days"
 - How will the network operate with mixed IPv4 and IPv6 routers?
- Two proposed approaches:
 - *Dual Stack*: some routers with dual stack (v6, v4) can "translate" between formats
 - *Tunneling*: IPv6 carried as payload in IPv4 datagram among IPv4 routers

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27

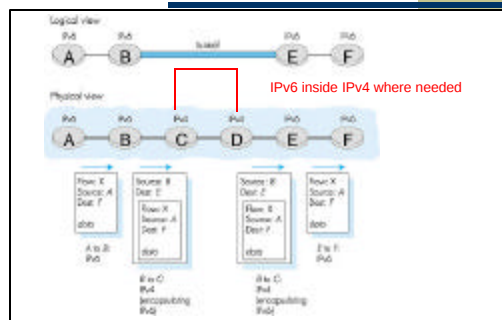
Dual Stack Approach



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28

Tunneling



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29

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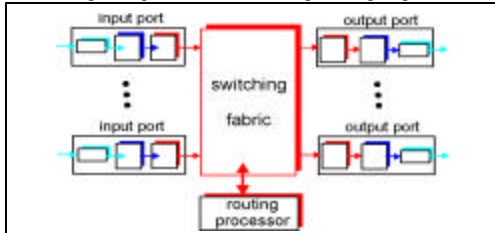
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30

Router Architecture Overview

Two key router functions:

- Run routing algorithms/protocol (RIP, OSPF, BGP)
- *Switching* datagrams from incoming to outgoing link



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What Does a Router Look Like?

- Line cards
 - Network interface cards
- Forwarding engine
 - Fast path routing (hardware vs. software)
 - Usually on line card
- Backplane
 - Switch or bus interconnect
- Processor
 - Handles routing protocols, error conditions

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32

Router Processing

- Packet arrives at inbound line card
- Header processed by forwarding engine
 - Forwarding engine determines output line card/destination
 - Checksum updated but not checked
- Packet copied to outbound line card
 - Odd situations sent to network processor

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33

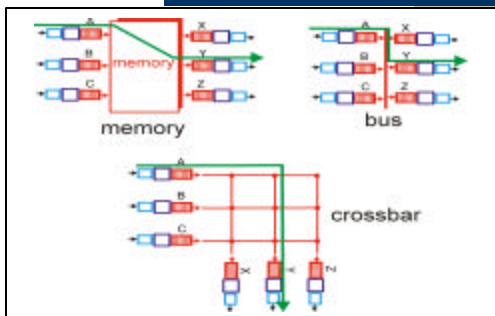
Network Processor

- Runs routing protocol and downloads forwarding table to forwarding engines
- Performs "slow" path processing
 - ICMP error messages
 - IP option processing
 - Fragmentation
 - Packets destined to router

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34

Three Types of Switching Fabrics



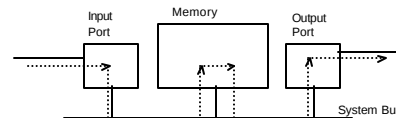
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35

Switching Via Memory

First generation routers:

- Packet copied by system's (single) CPU
- Speed limited by memory bandwidth (2 bus crossings per datagram)



Modern routers:

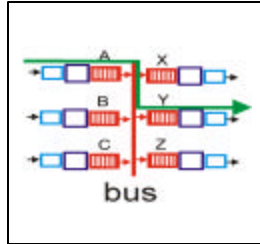
- Input port processor performs lookup, copy into memory
- Cisco Catalyst 8500

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36

Switching Via Bus

- Datagram from input port memory to output port memory via a shared bus
- Bus contention:** switching speed limited by bus bandwidth
- 1 Gbps bus, Cisco 1900: sufficient speed for access and enterprise routers (not regional or backbone)



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37

Switching Via An Interconnection Network

- Overcome bus bandwidth limitations
- Crossbar provides full NxN interconnect
 - Expensive
- Banyan networks, other interconnection nets initially developed to connect processors in multiprocessor
 - Typically less capable than complete crossbar
- Cisco 12000: switches Gbps through the interconnection network

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38

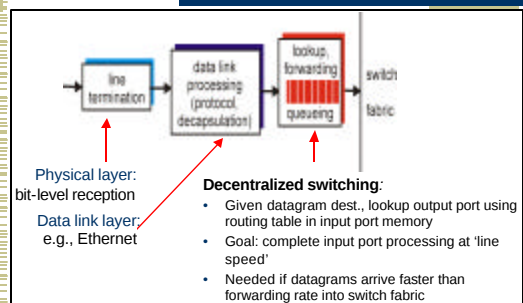
Switch Design Issues

- Suppose we have N inputs and M outputs
 - Multiple packets for same output – output contention
 - Switch contention – switching fabric cannot support arbitrary set of transfers
 - I.e., not a full crossbar
- Solution – buffer packets when/where needed
- What happens when these buffers fill up?
 - Packets are **THROWN AWAY!!** This is where packet loss comes from

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39

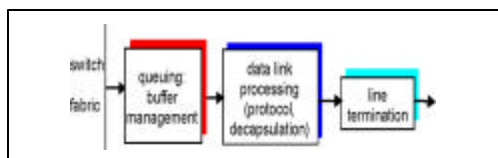
Input Port Functions



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40

Output Ports



- Queuing required when datagrams arrive from fabric faster than the line transmission rate

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41

Switch Buffering

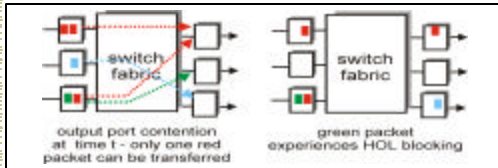
- 3 types of switch buffering
 - Input buffering
 - Fabric slower than input ports combined → queuing may occur at input queues
 - Can avoid any input queuing by making switch speed = $N \times$ link speed
 - Output buffering
 - Buffering when arrival rate via switch exceeds output line speed
 - Internal buffering
 - Can have buffering inside switch fabric to deal with limitations of fabric

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42

Input Port Queuing

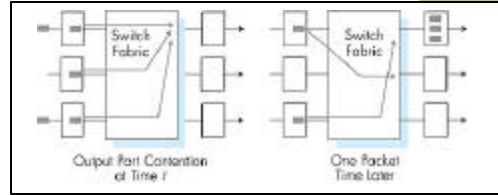
- Which inputs are processed each slot – schedule?
- Head-of-the-Line (HOL) blocking:** datagram at front of queue prevents others in queue from moving forward



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43

Output Port Queuing



- Scheduling discipline chooses among queued datagrams for transmission
 - Can be simple (e.g., first-come first-serve) or more clever (e.g., weighted round robin)

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44

Virtual Output Queuing

- Maintain per output buffer at input
- Solves head of line blocking problem
- Each of MxN input buffer places bid for output
- Challenge: map bids to schedule of interconnect transfers

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45

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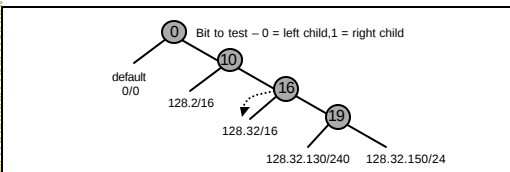
- IP Packet Format
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46

How To Do Variable Prefix Match

- Traditional method – Patricia Tree
 - Arrange route entries into a series of bit tests
- Worst case = 32 bit tests
 - Problem: memory speed is a bottleneck



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47

Speeding up Prefix Match - Alternatives

- Content addressable memory (CAM)
 - Hardware based route lookup
 - Input = tag, output = value associated with tag
 - Requires exact match with tag
 - Multiple cycles (1 per prefix searched) with single CAM
 - Multiple CAMs (1 per prefix) searched in parallel
- Ternary CAM
 - 0,1,don't care values in tag match
 - Priority (i.e. longest prefix) by order of entries in CAM

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48

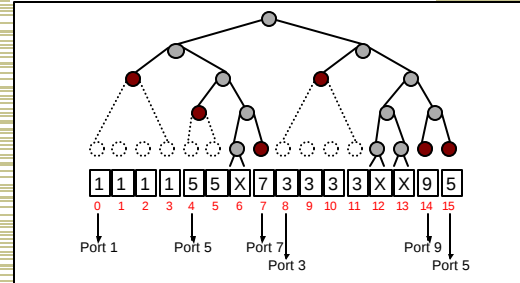
Speeding up Prefix Match

- Cut prefix tree at 16/24/32 bit depth
 - Fill in prefix tree entries by creating extra entries
 - Entries contain output interface for route
 - Add special value to indicate that there are deeper tree entries
 - Only keep 24/32 bit cuts as needed
- Example cut prefix tree at 16 bit depth
 - 64K entries!!
 - Use a variety of clever techniques to compress space taken

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49

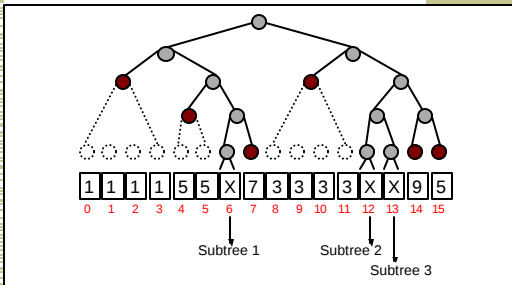
Prefix Tree



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50

Prefix Tree



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51

Speeding up Prefix Match

- Scaling issues
 - How would it handle IPv6
- Other possibilities
 - Why were the cuts done at 16/24/32 bits?

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52

Speeding up Prefix Match - Alternatives

- Route caches
 - Packet trains → group of packets belonging to same flow
 - Temporal locality
 - Many packets to same destination
- Other algorithms
 - Bremner-Barr – Sigcomm 99
 - Clue = prefix length matched at previous hop
 - Why is this useful?

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53